

arm

TZMP-1
Software
Reference
Implementation

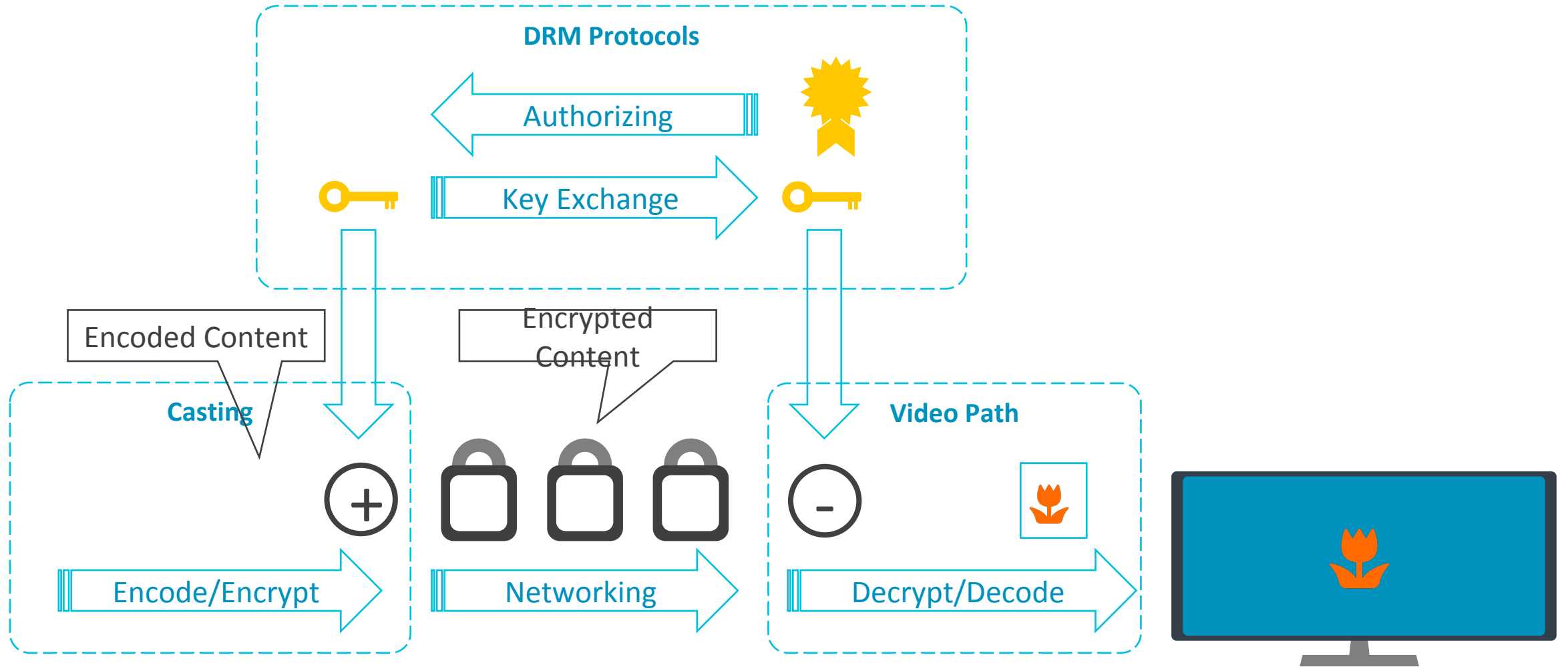
Ken Liu
2018-Mar-12

Content

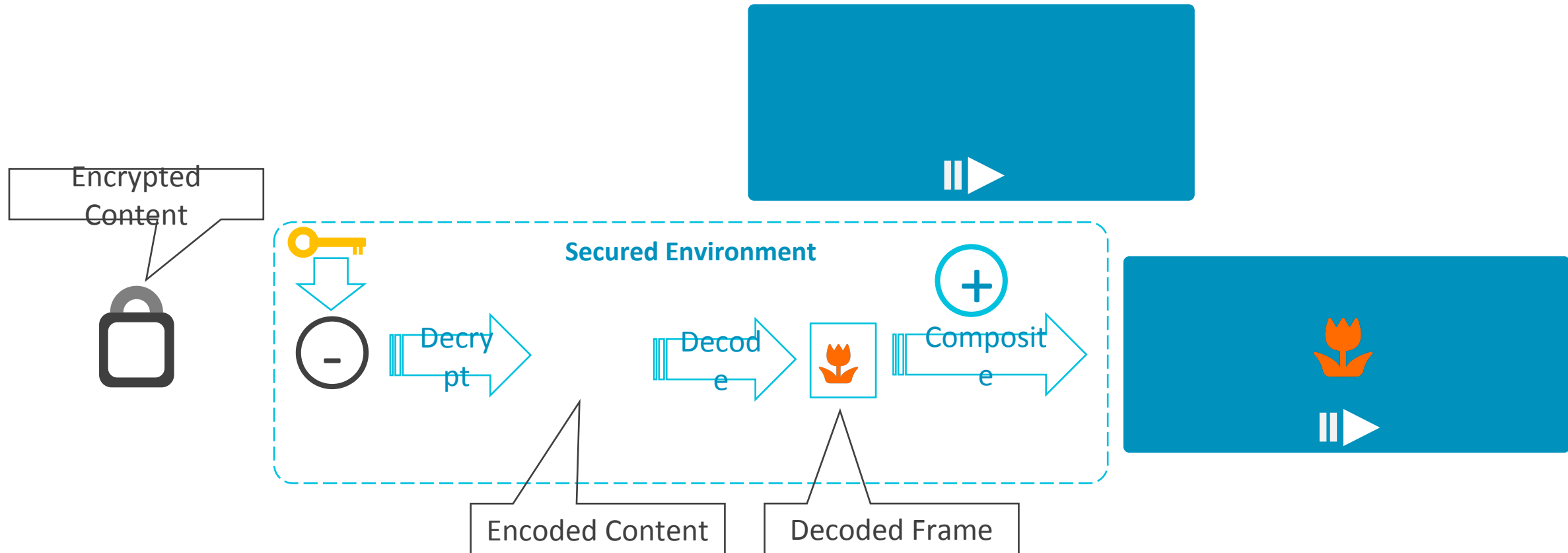
- **DRM Applications and Secure Video Path**

- Regular Secure Video Path Design with Trustzone
- TZMP1 Design Concepts
- Reference Implementation Details

General Process of DRM



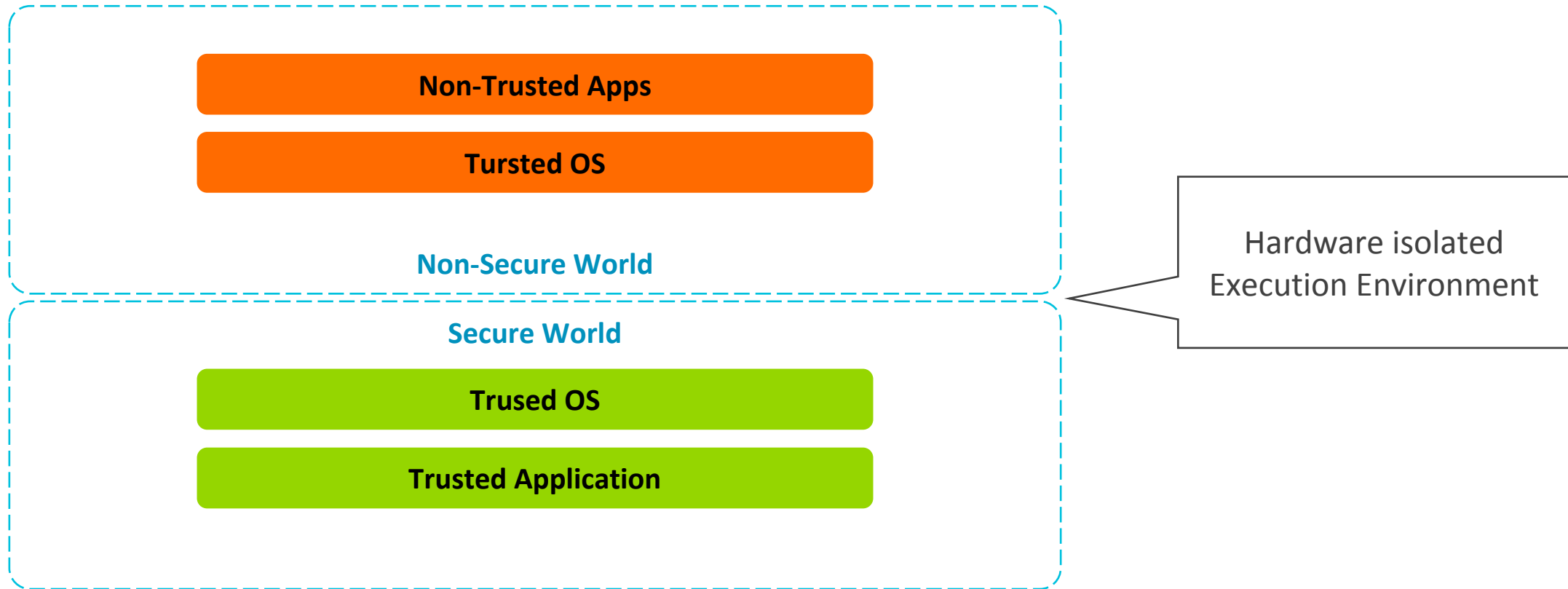
Ideal Model of Secure Video Path



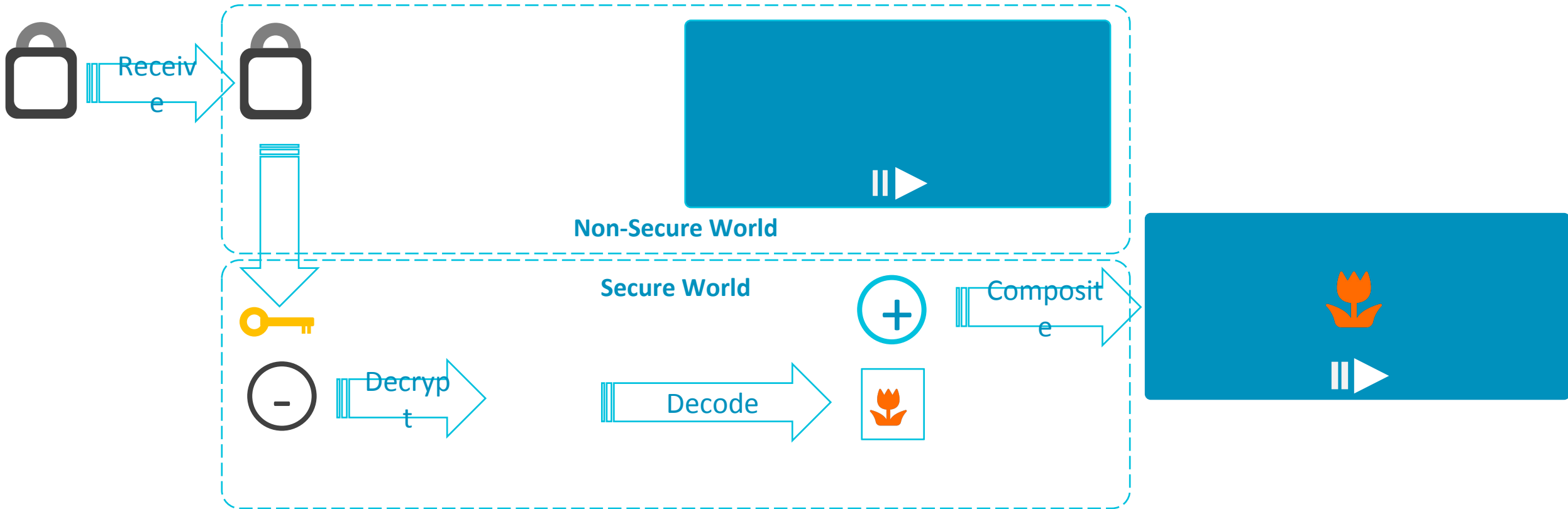
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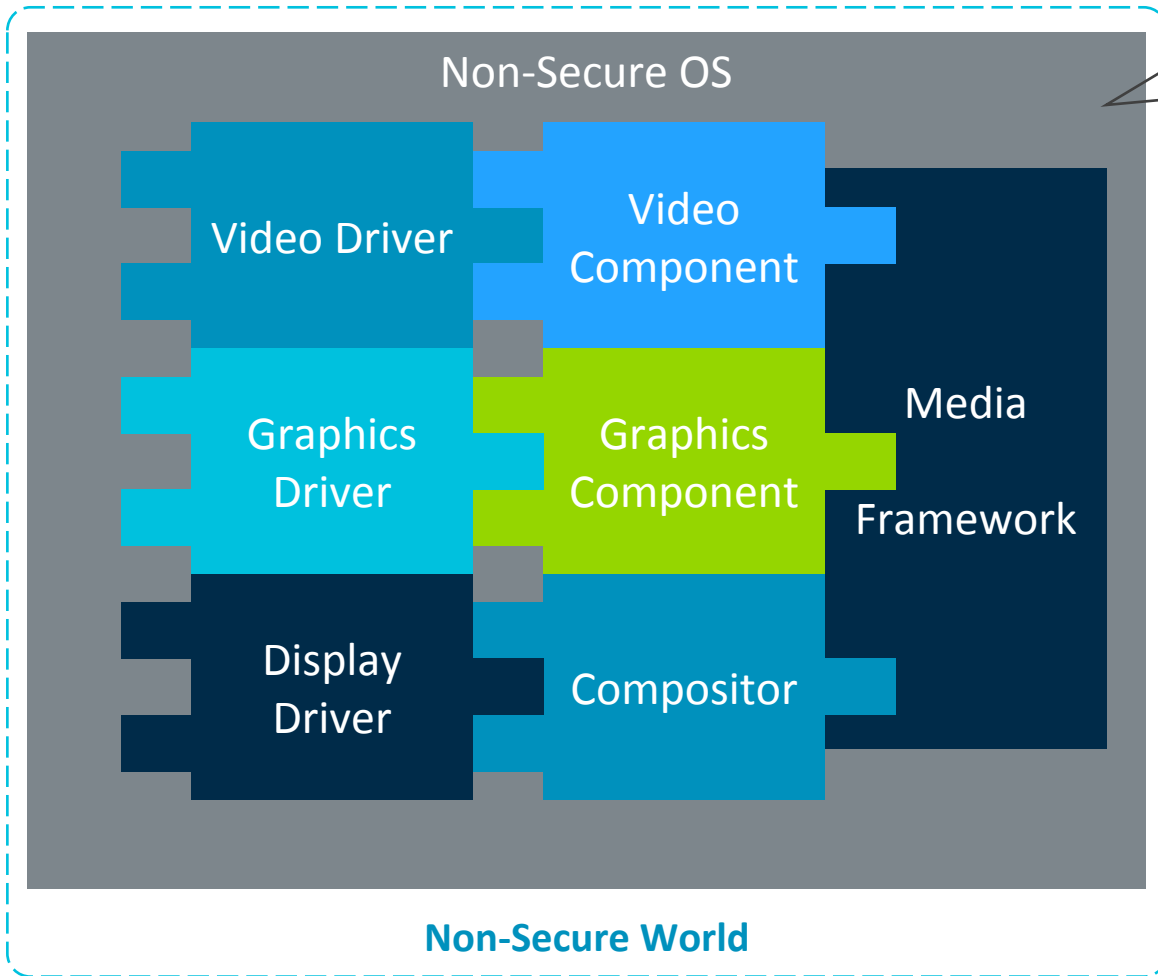
Arm Trustzone



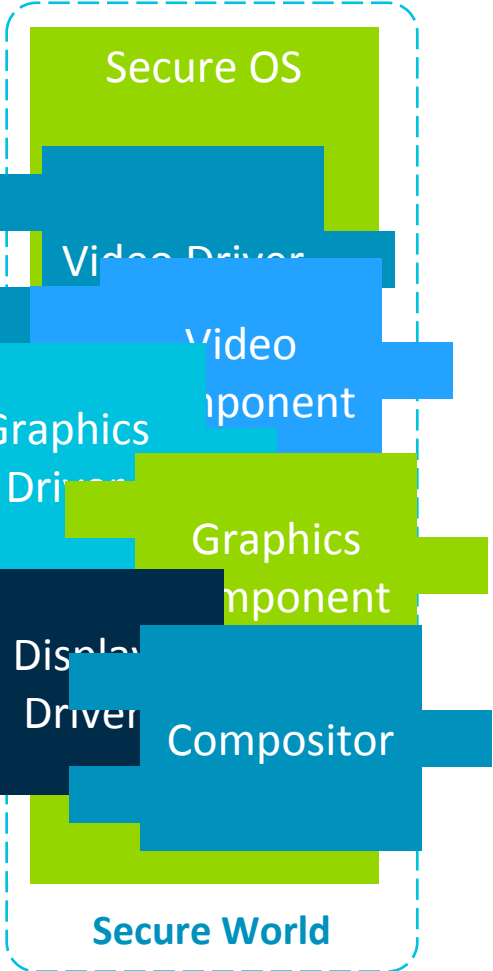
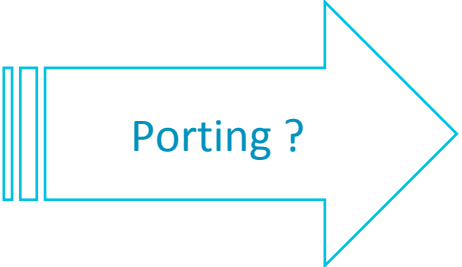
Regular Design with TrustZone



Issues of Regular Design

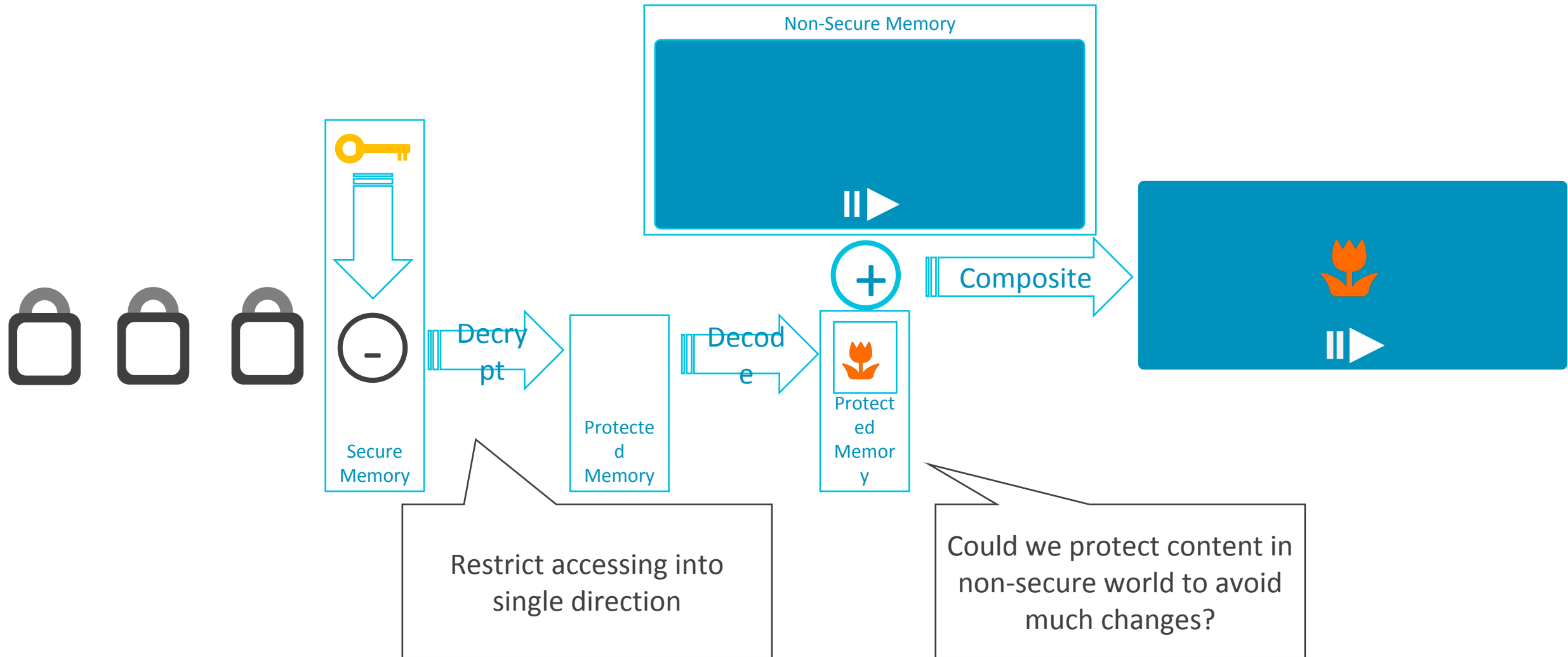


Mature multimedia frameworks in Non-Secure OS



Lack of API Runtime more attack interfaces

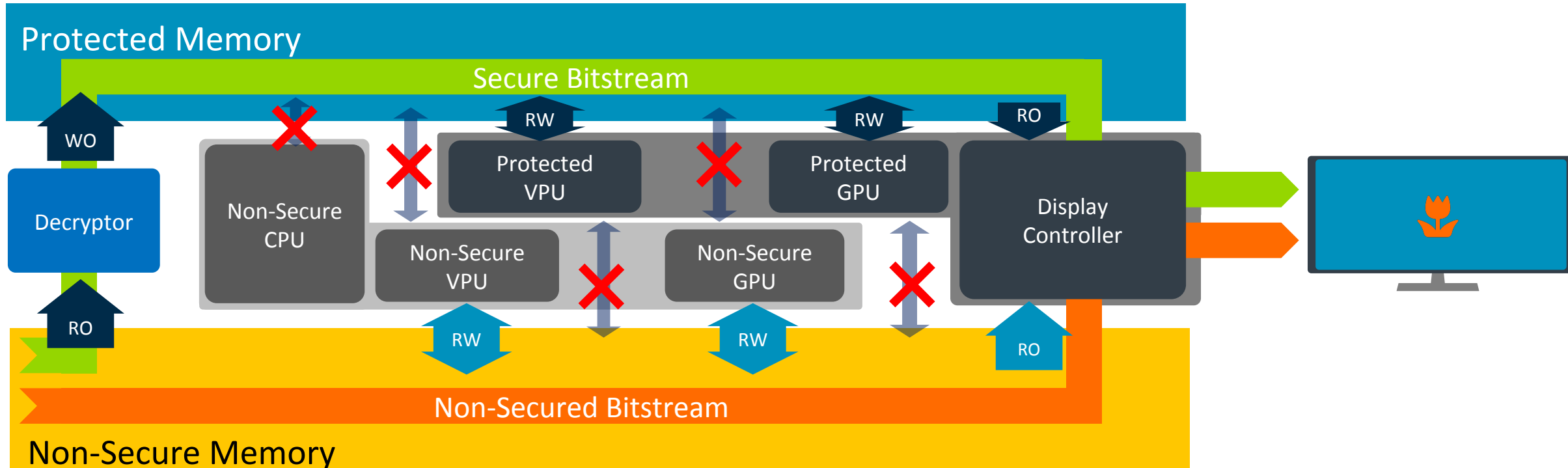
Protect Content in Non-Secure World



Content

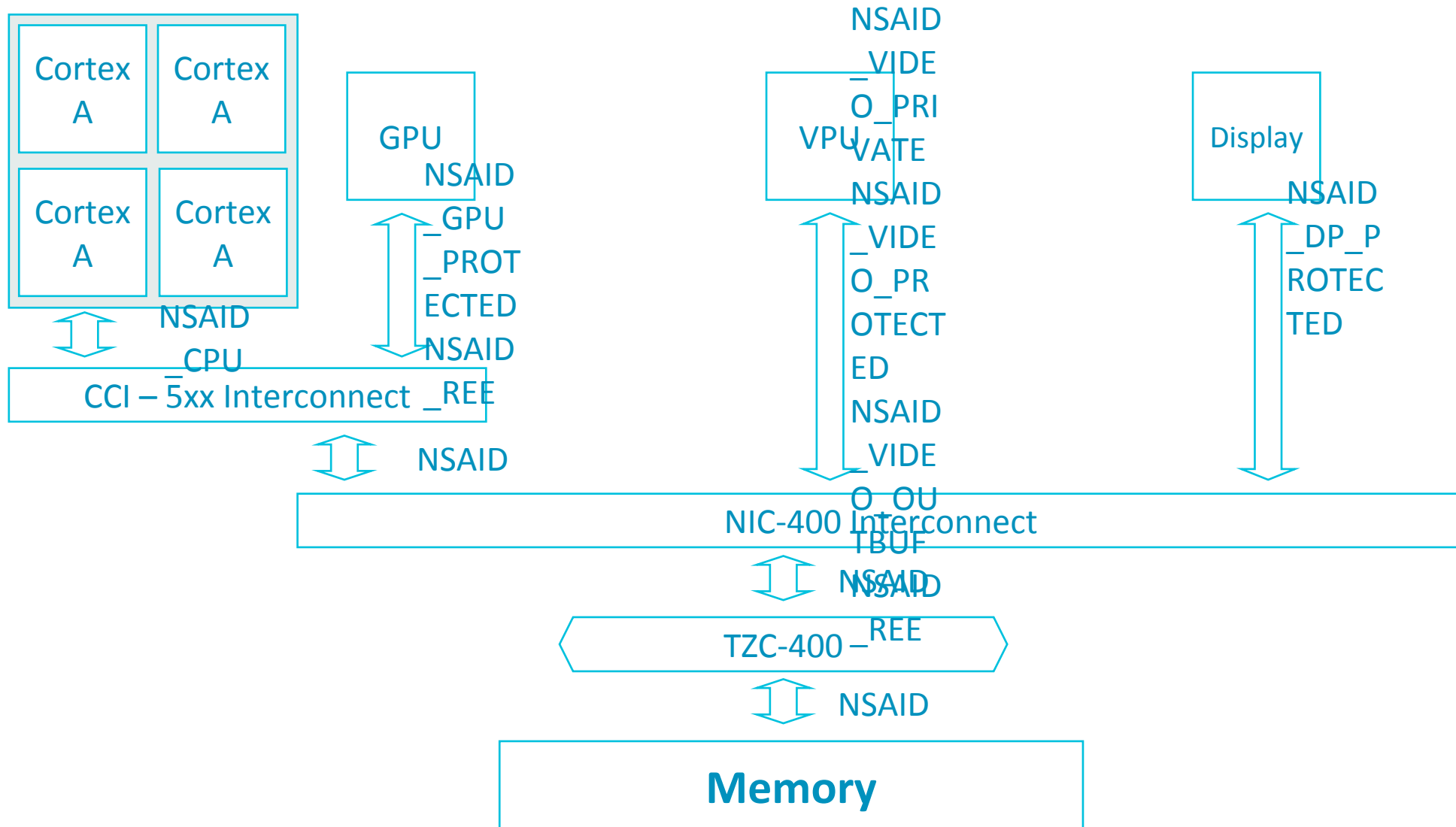
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Protected Memory and Secured Playback



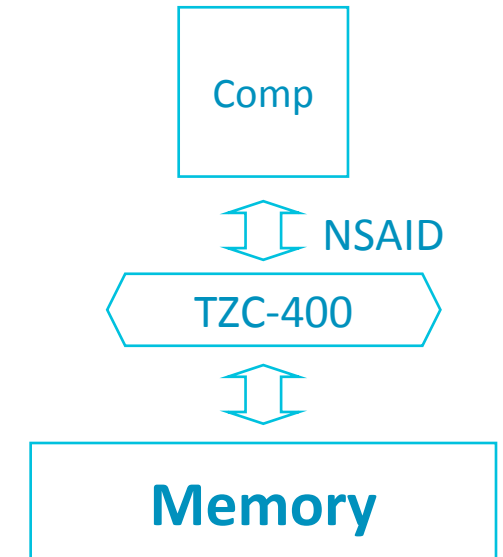
- Non-Secure Bitstream could **only** be accessed by Non-Secure hardware components
- Secure Bitstream could **only** be accessed by 'Protected' hardware components
- Display controller could read both types of bitstreams
- Mention the word 'Protected' – leads to Protected Memory and Protected Mode of HW

Hardware Architecture of TZMP1



Firewall of Accessing - Arm Trustzone Controller 400

Region	Ranges	NSAID 1	NSAID 2	...	NSAID 16	Secure Access
0	All Memory	RW Configurable	RW Configurable		RW Configurable	RW Configurable
1	Configurable	RW Configurable	RW Configurable		RW Configurable	RW Configurable
2	Configurable	RW Configurable	RW Configurable		RW Configurable	RW Configurable
3	Configurable	RW Configurable	RW Configurable		RW Configurable	RW Configurable
4	Configurable	RW Configurable	RW Configurable		RW Configurable	RW Configurable
5	Configurable	RW Configurable	RW Configurable		RW Configurable	RW Configurable
6	Configurable	RW Configurable	RW Configurable		RW Configurable	RW Configurable
7	Configurable	RW Configurable	RW Configurable		RW Configurable	RW Configurable



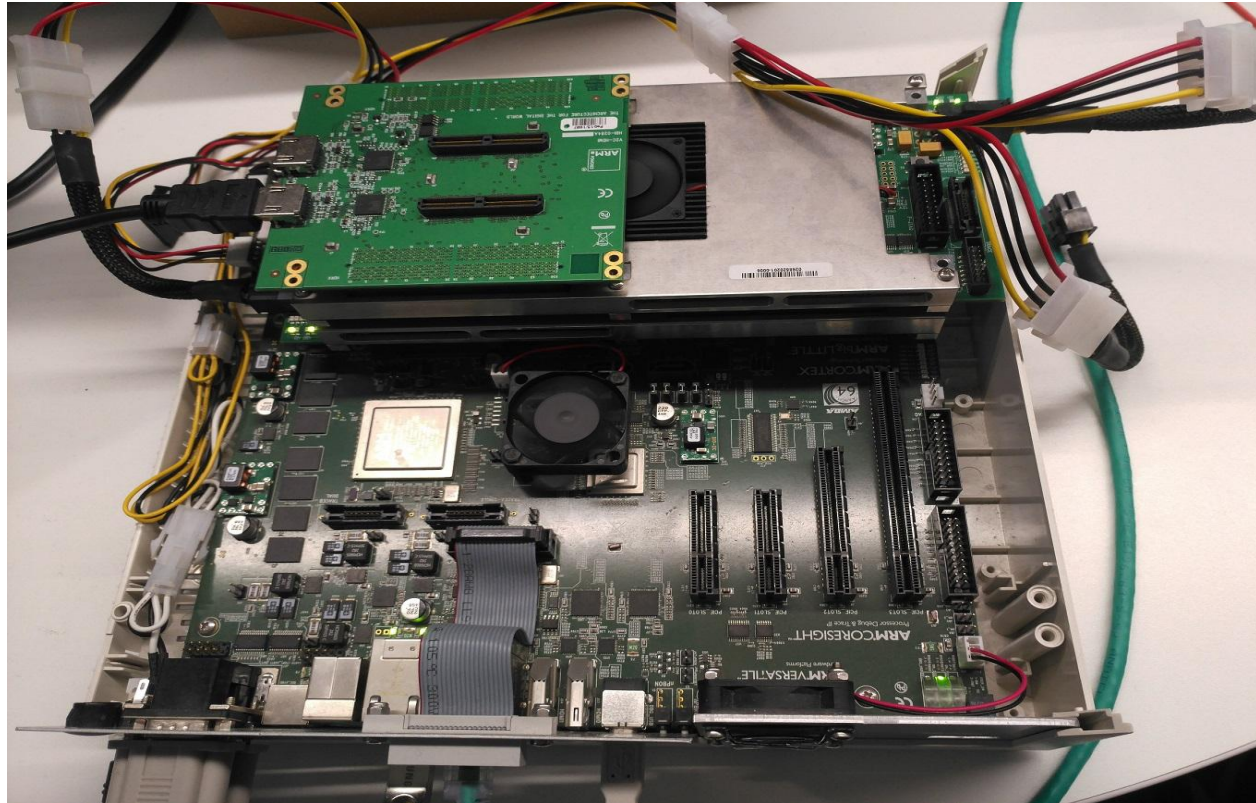
- Each non-secure memory accessing hardware is assigned with a Non-Secure Access ID (NSAID)
- TZC-400 checks NSAID and region permissions to decide access availability
- Total 8 regions and 16 NSAIDs are supported in TZC-400
- Secure accessing is also checked by TZC-400

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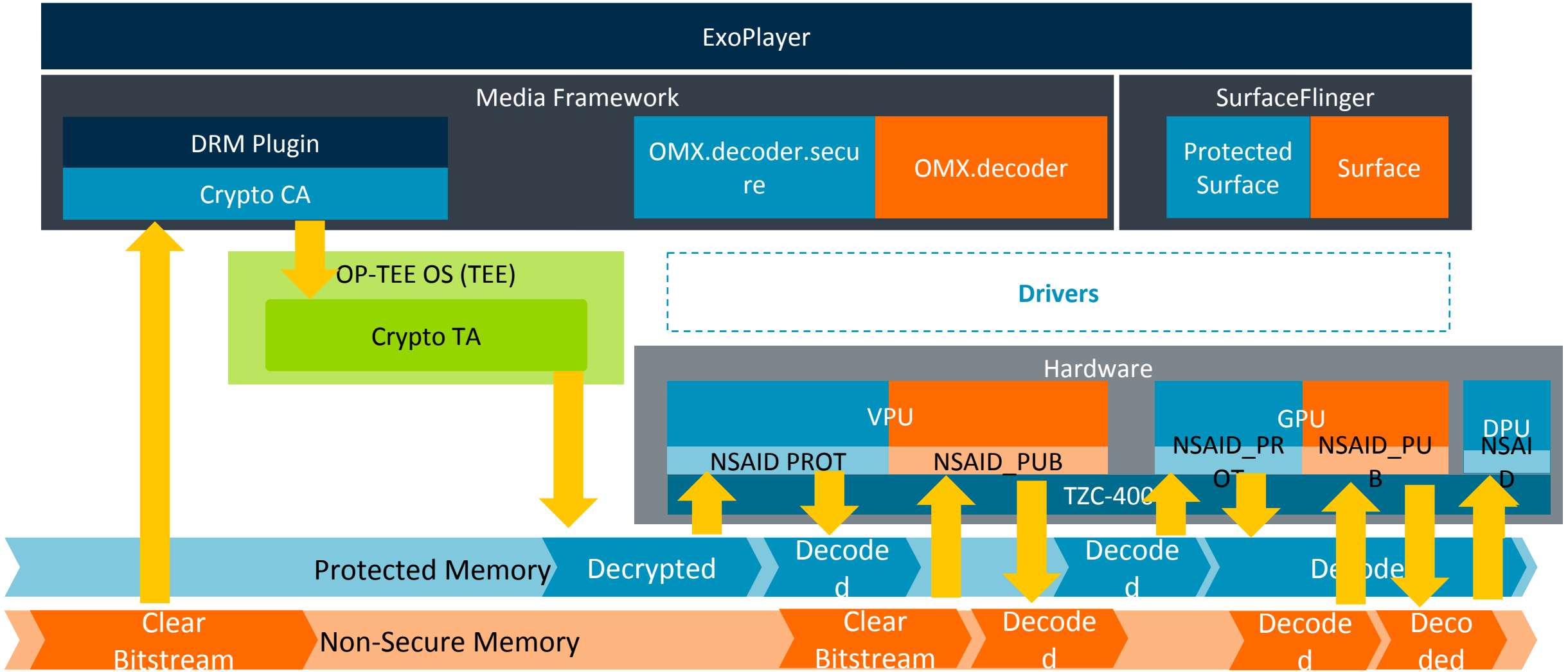
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Platform and Software Components

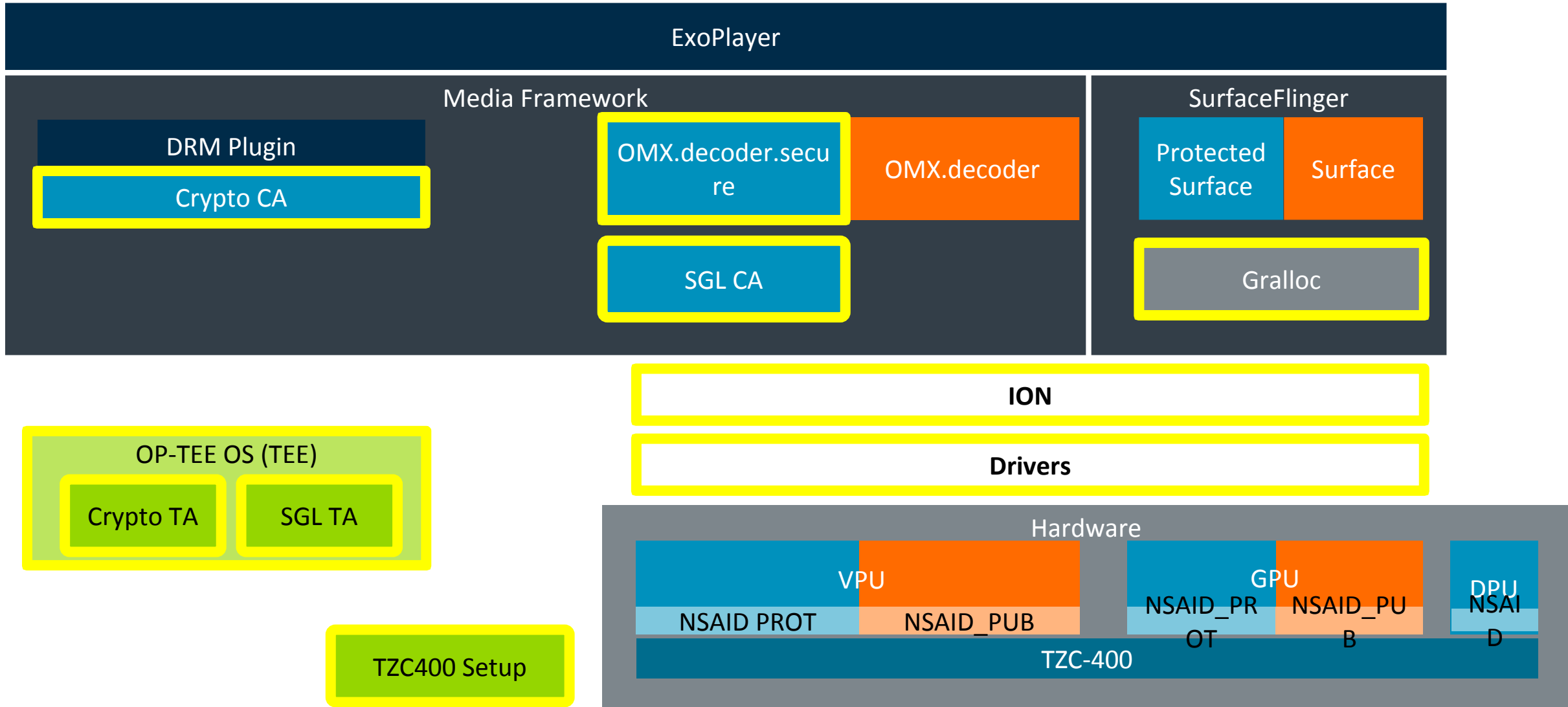


Component	Description
Board	Juno-r2 + 2 x Logictile
Non-Secure OS	Android lsk-4.4-arm64 Kernel
Secure OS	OPTEE OS
Boot	Arm-tf
Media IP	Arm Mali V550 G71 DP650
DRM	Clearkey / Widevine

Overall Reference Implementation



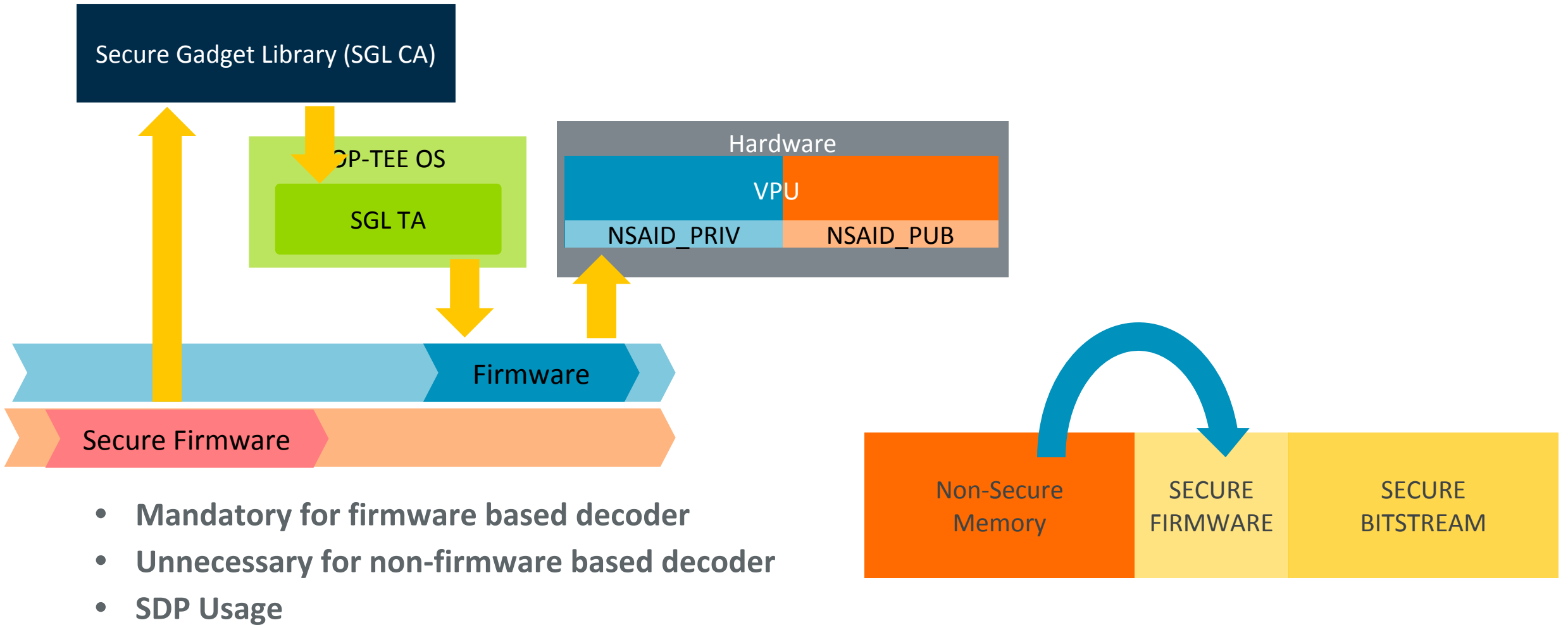
Required Software Modifications



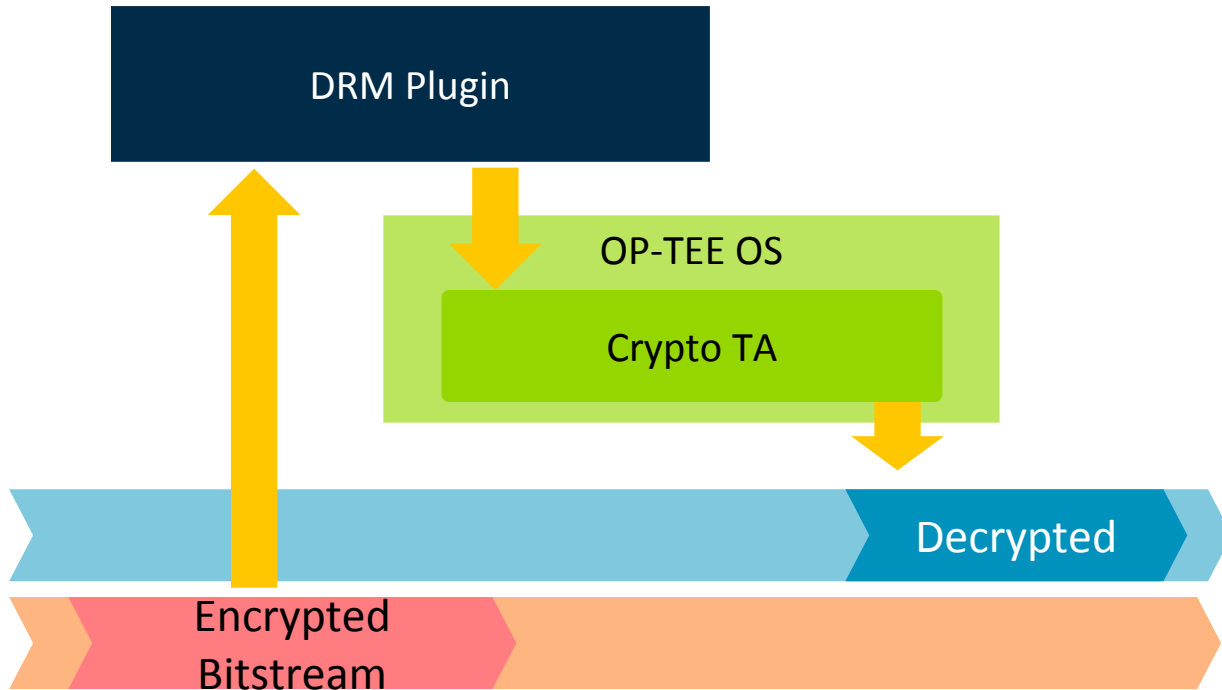
Memory Regions

Android (Linux)	Kernel Managed	ION::UNMAPPED HEAP		ION::CARVEOUT	TEE PARAM	X
Memory Regions	Non-Secure Memory	SECURE FIRMWARE	SECURE BITSTREAM	SECURE FRAME	TEE PARAM	Secure Memory
OPTEE OS	X	Secure Data Path		X	TEE PARAM	Runtime

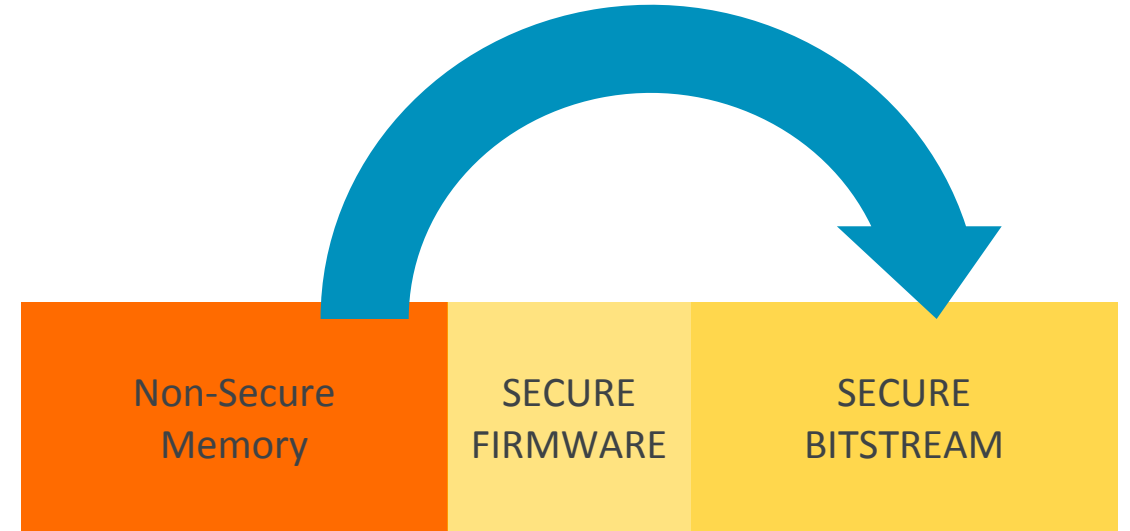
Secure VPU Firmware Loading



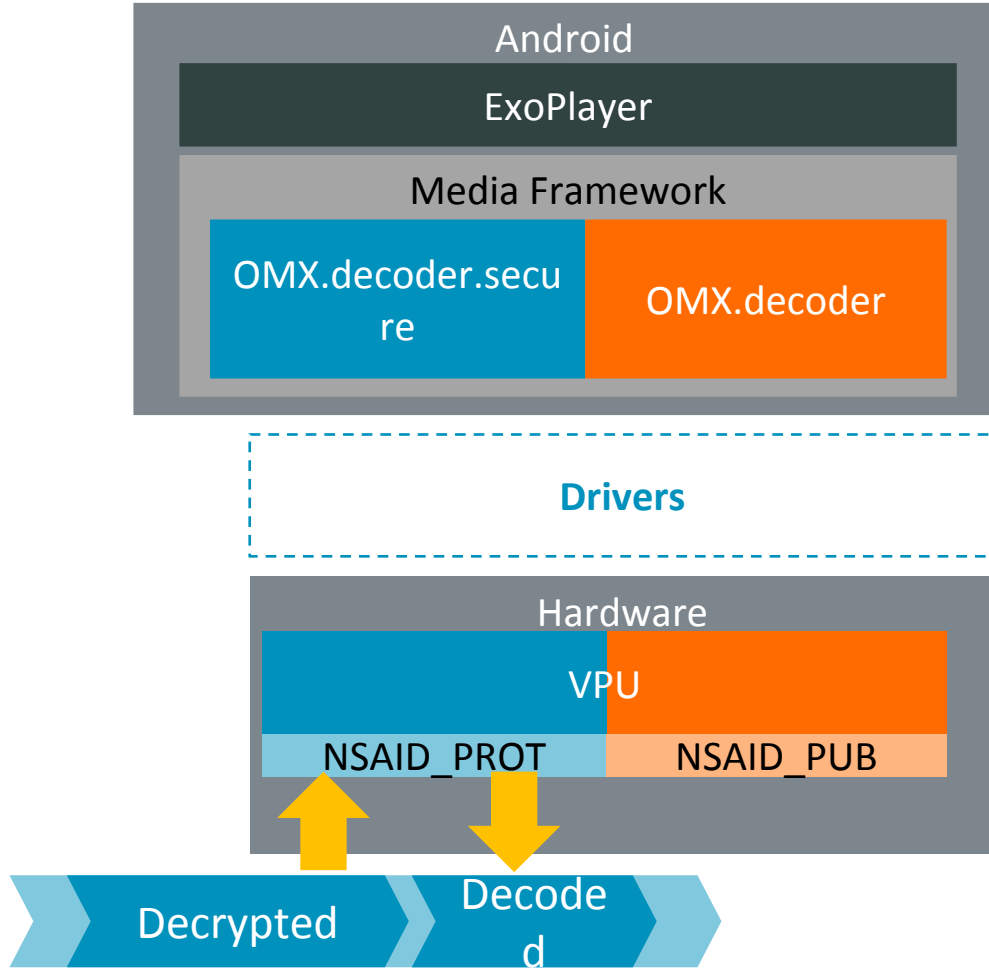
Adopt DRM Crypto



- Decrypt in OPTEE OS
- Put result into protected memory
- Take advantage SDP of OPTEE OS SDP



Adopt Secure Decoder



'Secure Video Path' in Android

Is DRM required 'Secure Codec' ?

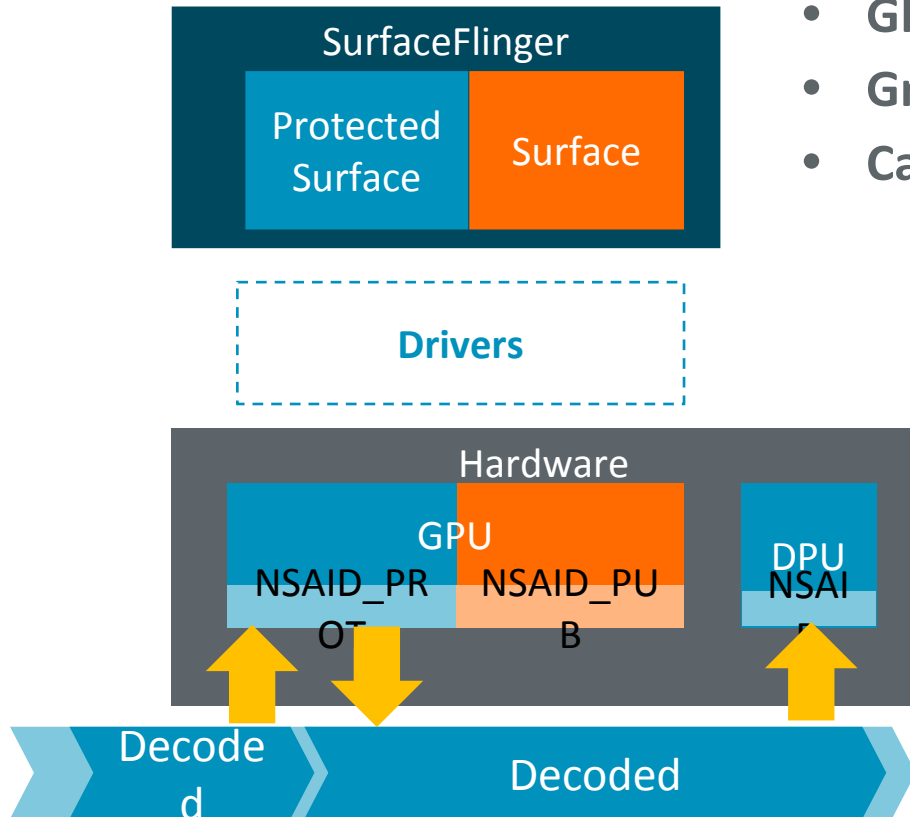
↳ Is 'Secure Codec' available ?

↳ Setup Secure Video Path

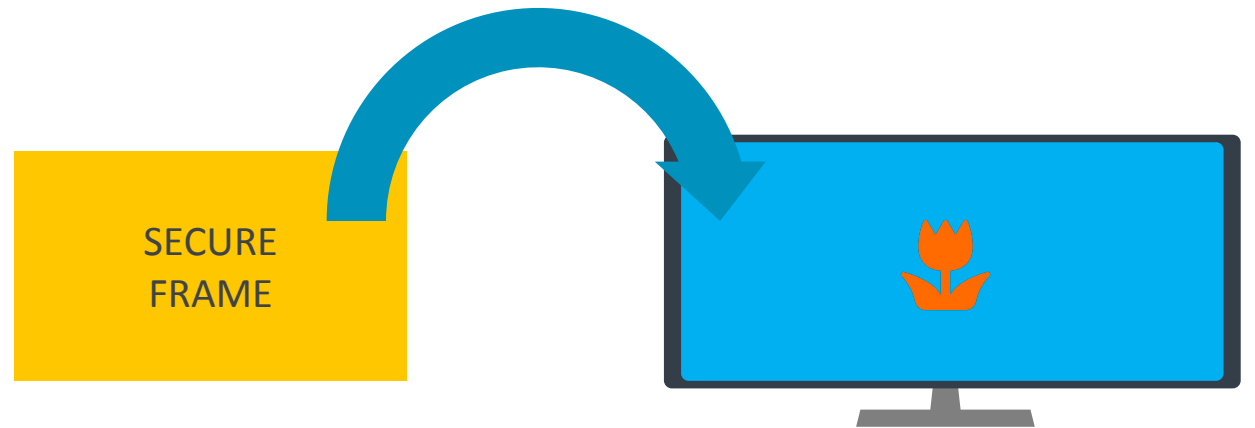
↳ Choose 'Secure Codec' component
Apply Protected Surface for output



Graphics and Display



- GPU and Display calls gralloc for surface buffers
- Gralloc allocates memory from specified buffer due to flags
- Call ION APIs for protected buffer



References

	Components	Repository
1	Workspace	To be upstreamed in April
2	Arm-tf	https://github.com/ARM-software/arm-trusted-firmware (Upstreaming)
3	OPTEE OS	https://github.com/OP-TEE/optee_os (Done)
4	Android manifest	To be upstreamed in April
5	Secure Gadget Library	Upstreaming in linaro private repository
6	Gralloc	https://developer.arm.com/products/software/mali-drivers/android-gralloc-module
7	Multimedia IP	Contact Arm support
8	Linux and DTS	https://git.linaro.org/landing-teams/working/arm/kernel-release.git (Upstreaming)
9	Arm Connected Community Page	Planned to be done by ~April

Thank You

Danke

Merci

谢谢

ありがとう

Gracias

Kiitos

감사합니다

धन्यवाद

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